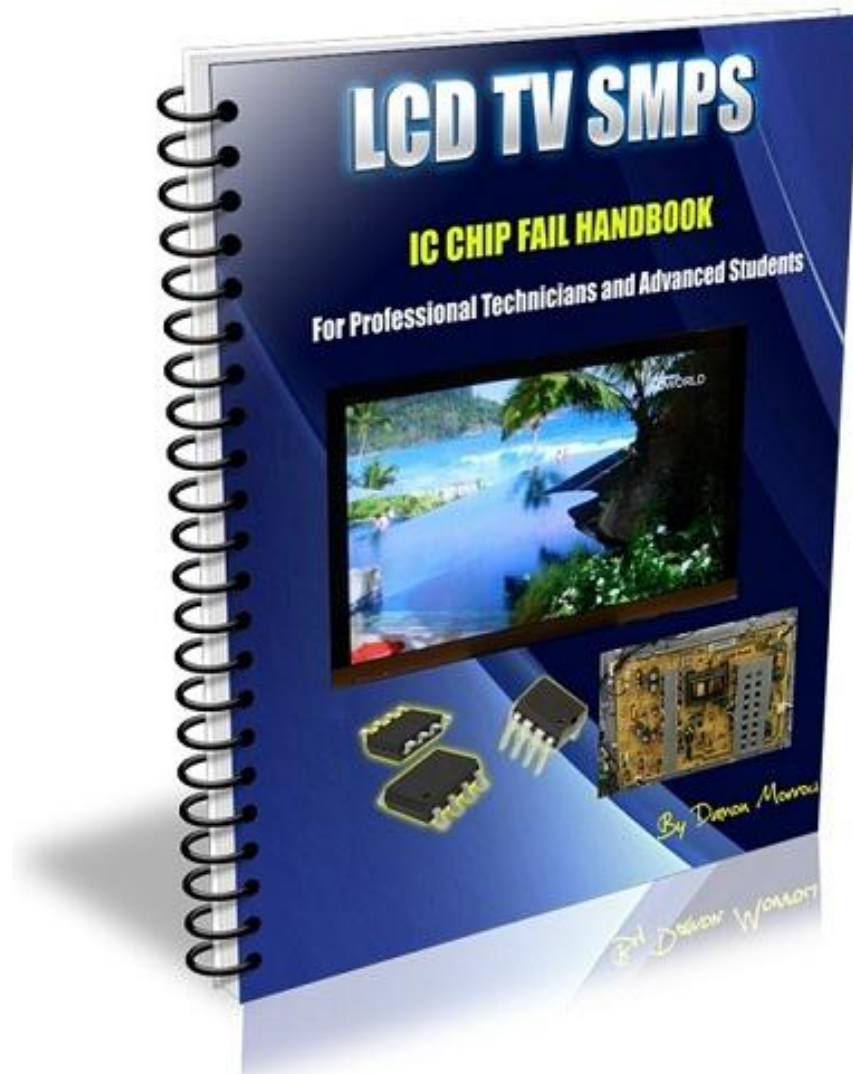


Preview Of The Ebook

LCD TV SMPS

IC CHIP FAIL HANDBOOK

For Professional Technicians and Advanced Students



Brought to you by Damon Morrow

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Dedication

This book is dedicated to: Jestine Yong, Sunny, David Maltz, Teonna Flags, and Michael Danish. I would like to give special thanks to Jestine for being a great teacher to me and a great friend and always inspiring me to study harder to become an Engineer of electronics. Also special thanks to David for being my big brother and keeping my spirits up and always encouraging me to stay fit and healthy and to go further and to never give up. Thank you 😊

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From Page 14

Let's move on with SMPS power boards:

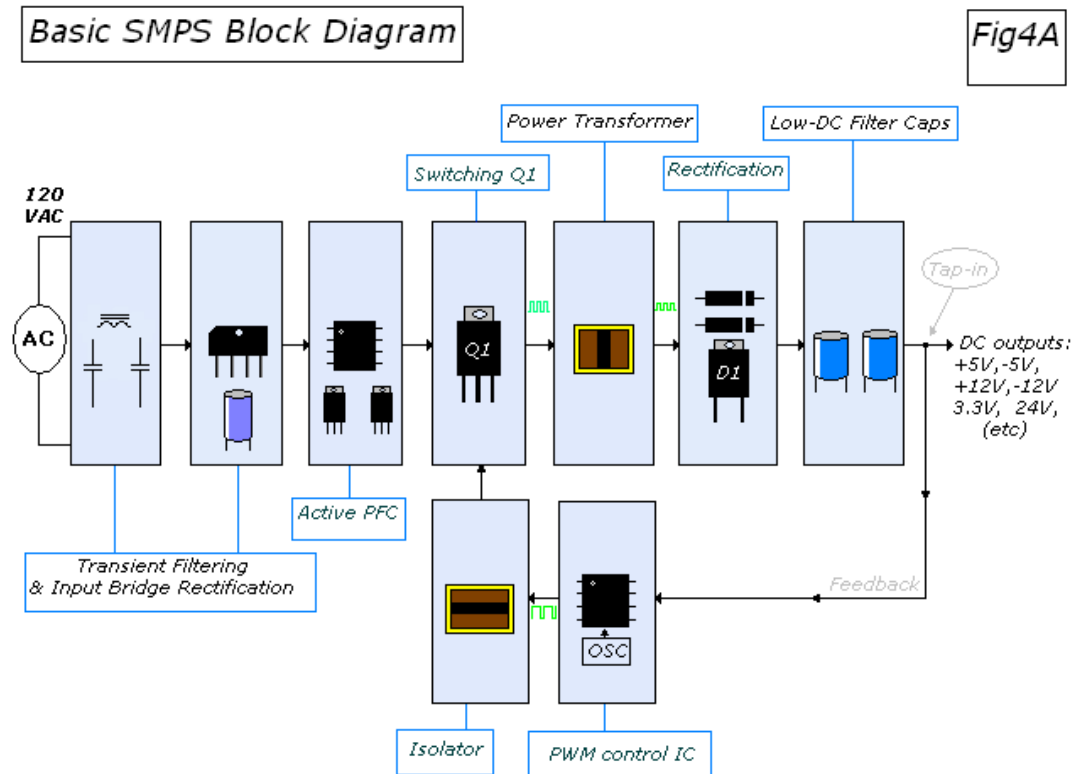
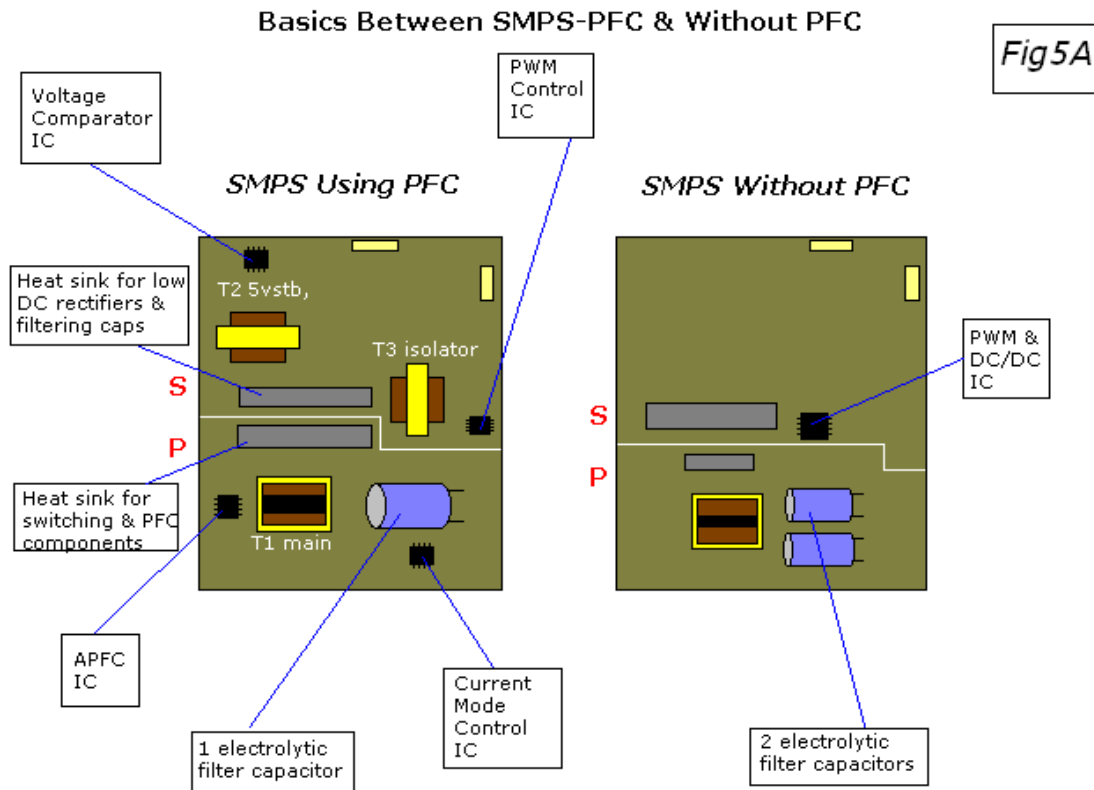


Fig4A illustrates a very basic diagram of a SMPS using an Active Power Factor Correction IC and a PWM IC, **NOTE** The PWM control IC can be integrated into another IC such as a PFC IC, or Mode Control IC,, it doesn't have to be a separate semi-conductor chip (as illustrated in *Fig4A*). However for learning purposes we will keep the PWM IC as an external function (separate IC) for now.

Some SMPS manufactures don't incorporate

From Page 17



Often on the secondary heat sink there will be several 3-pin power rectifiers each of which contain two internal power diodes, along with smaller electrolytic capacitors and energy coils in the low-DC filtering stage. The DC filtering stage filters the

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